



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

WARREN M. FARNWORTH
SALMAN AKRAM

Serial No.: 09/844,532

EXAMINER: Chang, R.

Filing Date: April 30, 2001

Art Unit: 3729

For: METHOD FOR FABRICATING A TEST INTERCONNECT
FOR BUMPED SEMICONDUCTOR COMPONENTS BY
FORMING RECESSES AND CANTILEVERED LEADS ON
A SUBSTRATE (AS AMENDED)

Attorney Docket No. 97-1433.1

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56
April 30, 2004

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Sir:

The citations listed on the accompanying Form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:

Stephen A. Gratton, Reg. No. 28,418
Attorney of Record

The Law Office of Stephen A. Gratton
2764 South Braun Way
Lakewood, CO 80228
Telephone: 303 989 6353

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

<i>Complete if Known</i>	
Application Number	09/844,532
Filing Date	04/30/2001
First Named Inventor	WARREN M. FARNWORTH
Art Unit	3729
Examiner Name	CHANG, R.
Attorney Docket Number	97-1433.1

U. S. PATENT DOCUMENTS

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